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SERIAL NUMBER 10/628,986	FILING DATE 07/28/2003 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 03-0593 81577
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APPLICANTS

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** CONTINUING DATA *N* *VS* ******* FOREIGN APPLICATIONS *N* *VS* *****

IF REQUIRED, FOREIGN FILING LICENSE

GRANTED

** 10/22/2003

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	<i>With Sole</i> <i>VS</i> Examiner's Signature Initials	WA	4	20	2

ADDRESS

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TITLE

Method of mapping logic failures in an integrated circuit die

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____
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